

## ABSTRACT

There are provided a balance adjustment circuit (6) for adjusting levels of first and second detection signals from a tracking detector, a  
5 differential circuit (8) for generating a difference signal between the adjusted first and second detection signals, an AD conversion circuit (10) for digitizing the difference signal, a wobble signal detection circuit (14) for detecting a wobble signal from the digitized difference signal, an adder circuit (30) for generating a sum signal of the adjusted first and second detection signals, a  
10 binarization circuit (32) for converting the sum signal into a binarized signal, a latch circuit (33) for latching the binarized signal and converting the same into a timing signal, a control signal generation circuit (34) for generating a control signal based on the timing signal and the digitized difference signal, a residual component removal circuit (18) for removing a residual signal  
15 component included in the digitized difference signal based on the control signal and outputting a LPP detection signal, and an address detection circuit (21) for detecting address information from the LPP detection signal. The wobble signal and the LPP signal are detected reliably with a simple configuration.